UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,600,221 B1
APPLICATION NO. : 10/679939
DATED : October 6, 2009
INVENTOR(S) : Achutha Raman Rangachari

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 36 line 46
After "trace completes execution; and"

Insert:

-- a squash circuit coupled to receive a signal from the comparator circuit identifying detection of an out-of-order memory dependency condition, the squash circuit: --

Signed and Sealed this
Eighteenth Day of January, 2011

David J. Kappos

David J. Kappos
Director of the United States Patent and Trademark Office